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APPLICATION NO. FILING DATE		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
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24247	7590	11/21/2005		EXAMINER		
TRASK			GRAYBILL, DAVID E			
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			2822			
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Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	on No.	Applicant(s)				
		09/832,16	00	AKRAM ET AL.	(An)			
	Office Action Summary	Examiner		Art Unit	- (M)			
		David E. C	3raybill	2822				
Period fo	The MAILING DATE of this communic or Reply	ation appears on the	cover sheet with the c	orrespondence addr	ess			
WHIC - External after - If NO - Failu Any	ORTENED STATUTORY PERIOD FOR CHEVER IS LONGER, FROM THE MAN IN THE MAIN IN THE	ALING DATE OF THE STATE OF THE	IIS COMMUNICATION ent, however, may a reply be tim Il expire SIX (6) MONTHS from ication to become ABANDONE	N. nely filed the mailing date of this comr D (35 U.S.C. § 133).				
Status								
2a)□	Responsive to communication(s) filed This action is FINAL . 28 Since this application is in condition for closed in accordance with the practice	o)⊠ This action is n or allowance except	on-final. for formal matters, pro		nerits is			
Dispositi	ion of Claims							
5)□ 6)⊠ 7)□ 8)□ Applicati 9)□ 10)□	Claim(s) 1-9 and 12-34 is/are pending 4a) Of the above claim(s) 4,9 and 23-3 Claim(s) is/are allowed. Claim(s) 1-3,5-8 and 12-22 is/are rejected to. Claim(s) is/are objected to. Claim(s) are subject to restriction are subject to restriction. The specification is objected to by the The drawing(s) filed on is/are: Applicant may not request that any object Replacement drawing sheet(s) including the oath or declaration is objected to	ected. on and/or election re Examiner. a) accepted or b) ion to the drawing(s) bhe correction is require	equirement. objected to by the Ele held in abeyance. See led if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CFR	• •			
,	•	by the Examiner. No	ne the attached Office	Action of foliar 10	-102.			
Priority under 35 U.S.C. § 119 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some color None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.								
2) Notic 3) Inform	t(s) e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PT mation Disclosure Statement(s) (PTO-1449 or P r No(s)/Mail Date		4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	nte	52)			

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In view of the appeal brief filed on 9-14-5, PROSECUTION IS HEREBY REOPENED. New grounds of rejection are set forth below.

To avoid abandonment of the application, appellant must exercise one of the following two options:

- (1) file a reply under 37 CFR 1.111 (if this Office action is non-final) or a reply under 37 CFR 1.113 (if this Office action is final); or,
- (2) initiate a new appeal by filing a notice of appeal under 37 CFR 41.31. A new notice of appeal fee and appeal brief fee will not be required for applicant to appeal from the new Office action. Any appeal brief filed on or after September 13, 2004 must comply with 37 CFR 41.37.

In the rejections infra, generally, reference labels are recited only for the first recitation of identical claim elements.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

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(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claims 1-3, 5-8, 12, 13, 15-17, 19 and 20 are rejected under 35 U.S.C. 102(e) as being clearly anticipated by Crumly (5946555).

At column 1, line 49 to column 2, line 33, Crumly discloses a method for fabricating a chip-scale package, comprising: positioning a preformed polymeric film 18 including at least one aperture 27 that extends substantially longitudinally therethrough over a semiconductor device 12 with the at least one aperture in substantial alignment with a corresponding bond pad 14 of the semiconductor device; and selectively introducing conductive material 32 into the at least one aperture; adhering the

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preformed polymeric film to the semiconductor device; defining at least another aperture 27 through the preformed polymeric film; wherein the defining is effected before the positioning; wherein the introducing comprises bonding the conductive material to the corresponding bond pad; wherein the introducing comprises depositing the conductive material onto the preformed polymeric film and within the at least one aperture; wherein depositing comprises physical vapor depositing "sputtering" the conductive material; forming at least one contact 20 at an end of the conductive material, opposite the semiconductor device; placing a conductive structure 22 adjacent the at least one contact; positioning at least one conductive trace 22 on the preformed polymeric film and in communication with the conductive material; forming at least one contact 22 in communication with the conductive trace; placing a conductive structure adjacent the at least one contact 20; placing the preformed polymeric film on at least a portion of a peripheral edge of the semiconductor device; and placing polymeric material 28 at least laterally adjacent the conductive structure.

Claims 1-3, 5-7 and 12-22 are rejected under 35 U.S.C. 102(e) as being clearly anticipated by Fjelstad (6284563).

At column 3, line 43 to column 4, line 54; column 8, line 13 to column 9, line 19; and column 11, lines 16-41, Fjelstad discloses a method for fabricating a chip-scale package, comprising: positioning a preformed

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polymeric film 130 including at least one aperture "contact holes" that extends substantially longitudinally therethrough over a semiconductor device 100 with the at least one aperture in substantial alignment with a corresponding bond pad 110 of the semiconductor device; and selectively introducing conductive material 170 into the at least one aperture; adhering the preformed polymeric film to the semiconductor device; defining at least another aperture "contact holes" through the preformed polymeric film; wherein the defining is effected before the positioning; wherein the introducing comprises bonding the conductive material to the corresponding bond pad; wherein the introducing comprises depositing the conductive material onto the preformed polymeric film and within the at least one aperture; forming at least one contact 175 at an end of the conductive material, opposite the semiconductor device; placing a conductive structure "solder" adjacent the at least one contact; wherein placing comprises applying solder to the at least one contact; positioning at least one conductive trace 170 on the preformed polymeric film and in communication with the conductive material; forming at least one contact 175 in communication with the conductive trace; placing a conductive structure adjacent the at least one contact; wherein placing comprises applying solder to the at least one contact; placing the preformed polymeric film on at least a portion of a peripheral edge of the semiconductor device; placing

polymeric material 180 at least laterally adjacent the conductive structure; placing an inherently conductive (at least thermally) elastomer 290 over at least one conductive structure; placing another inherently conductive (at least thermally) structure 180' in contact with the conductive elastomer, opposite the at least one conductive structure.

Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Fjelstad as applied to claim 7, and further in combination with Crumly (5946555).

Fjelstad does not appear to explicitly disclose wherein depositing comprises chemical vapor depositing or physical vapor depositing the conductive material.

Nonetheless, as cited, Fjelstad discloses wherein depositing comprises "a variety of techniques, such as by electroplating or by electroless plating." Additionally, as cited supra, Crumly discloses that electroplating "electrolysis" and physical vapor depositing "sputtering" are alternatives and equivalents; therefore, it would have been obvious to substitute or combine the physical vapor depositing of Crumly for or with the electroplating of Fjelstad. See In re May (CCPA) 136 USPQ 208 (It is our opinion that the substitution of Wille's type seal for the cement of Hallauer in Figure 1 would be obvious to persons of ordinary skill in the art from the disclosures of these references, merely involving an obvious selection between known

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alternatives in the art and the application of routine technical skills.); In re Cornish (CCPA) 125 USPQ 413; In re Soucy (CCPA) 153 USPQ 816; Sabel et al. v. The Wickes Corporation et al. (DC SC) 175 USPQ 3; Ex parte Seiko Koko Kabushiki Kaisha Co. (BdPatApp&Int) 225 USPQ 1260; and Ex parte Rachlin (BdPatApp&Int) 151 USPQ 56. See also Smith v. Hayashi, 209 USPQ 754 (Bd. of Pat. Inter. 1980) (However, there was evidence that both phthalocyanine and selenium were known photoconductors in the art of electrophotography. "This, in our view, presents strong evidence of obviousness in substituting one for the other in an electrophotographic environment as a photoconductor." 209 USPQ at 759.). An express suggestion to substitute one equivalent component or process for another is not necessary to render such substitution obvious. In re Fout, 675 F.2d 297, 213 USPQ 532 (CCPA 1982). "It is prima facie obvious to combine two compositions each of which is taught by the prior art to be useful for the same purpose, in order to form a third composition to be used for the very same purpose.... [T]he idea of combining them flows logically from their having been individually taught in the prior art." In re Kerkhoven, 626 F.2d 846, 850, 205 USPQ 1069, 1072 (CCPA 1980) (citations omitted). See also In re Crockett, 279 F.2d 274, 126 USPQ 186 (CCPA 1960); Ex parte Quadranti, 25 USPQ2d 1071 (Bd. Pat. App. & Inter. 1992).

Claims 14, 18, 21 and 22 are rejected under 35 U.S.C. 103(a) as being unpatentable over Crumly as applied to claims 13 and 17, and further in combination with Fjelstad (6284563).

Crumly does not appear to explicitly disclose wherein placing comprises applying solder to the at least one contact; placing a conductive elastomer over at least one conductive structure; placing another conductive structure in contact with the conductive elastomer, opposite the at least one conductive structure.

Nevertheless, as cited, Fjelstad discloses wherein placing comprises applying "solder" to the at least one contact; placing an inherently conductive (at least thermally) elastomer 290 over at least one conductive structure; placing another inherently conductive (at least thermally) structure 180' in contact with the conductive elastomer, opposite the at least one conductive structure. Furthermore, it would have been obvious to combine this disclosure of Fjelstad with the disclosure of Crumly because it would facilitate low stress external electrical connection.

Claims 21 and 22 are rejected under 35 U.S.C. 103(a) as being unpatentable over Crumly, Fjelstad or Fjelstad and Crumly as applied to claims 1-3, 5-8 and 12-17, and further in combination with Jacobs (6294407).

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The applied prior art does not appear to disclose literally, placing a conductive elastomer over at least one conductive structure; placing another conductive structure in contact with the conductive elastomer, opposite the at least one conductive structure.

Regardless, at column 5, line 61 to column 6, line 46; and column 14, line 42 to column 15, line 2, Jacobs discloses placing a conductive (at least thermally) elastomer 106 over at least one conductive structure 104, and placing another conductive structure 112a in contact with the conductive elastomer, opposite the at least one conductive structure. Moreover, it would have been obvious to combine this disclosure of Jacobs with the disclosure of the applied prior art because it would enable external electrical connection and package cooling.

Applicant's remarks filed 9-14-5 have been fully considered and are most in view of the new grounds of rejection.

The art made of record and not applied to the rejection is considered pertinent to applicant's disclosure. It is cited primarily to show inventions relevant to the examination of the instant invention.

For information on the status of this application applicant should check PAIR: Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Alternatively, applicant may contact the File Information Unit at (703) 308-2733. Telephone status inquiries should not be directed to the examiner. See MPEP 1730VIC, MPEP 203.08 and MPEP 102.

Any other telephone inquiry concerning this communication or earlier communications from the examiner should be directed to David E. Graybill at (571) 272-1930. Regular office hours: Monday through Friday, 8:30 a.m. to 6:00 p.m.

The fax phone number for group 2800 is (571) 273-8300.

David E. Graybill Primary Examiner Art Unit 2822

D.G. 17-Nov-05